

- Project based
- Guidance provided
- Students develop a fully functional embedded system
- Module based

Location:

Analog Devices Romania Office, Cluj-Napoca

Duration: 6 weeks, 8h/day

MODULES

- *Y* HARDWARE
- **MIL-FPGA DESIGN**
- **MEMBEDDED SOFTWARE**
- **MAPPLICATIONS SOFTWARE**

IMPORTANT DATES

- **⊘** Registration deadline: **April 15th**
- Results sent out: by May 15th
- Period: July 14th August 22nd

HOW TO APPLY?

Fill out the registration form:

